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Substitute Form PTO-1449 (Modified) U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 06666-077001	Application No. 09/848,778
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))	Applicant Beerel, et al.	
	Filing Date May 3, 2001	Group Art Unit 2661

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U.S. Patent Documents

Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
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	AD						
	AE						
	AF						
	AG						
	AH						
	AI						

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation Yes	Translation No
	AJ							
	AK							
	AL							
	AM							
	AN							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AO	Forney, "The Forward-Backward Algorithm", Proceedings of the Thirty-fourth Annual Allerton Conference on Communication, Control and Computing, October 2-October 4, 1996, pp. 432-446
	AP	Aji, et al., "The Generalized Distributive Law", IEEE Trans. Inform. Theory, 46(2):325-343, March 2000
	AQ	Cormen, et al., "Introduction to Algorithms", The MIT Press, Cambridge, Mass., 1990
	AR	Brent, et al., "A Regular Layout for Parallel Adders", IEEE Transactions on Computers, C-31:260-264, March 1982
	AS	Despain, Chapter 2: "Notes on computer architecture for high performance", in New Computer Architectures, Academic Pres, 1984

Examiner Signature	Date Considered 2/12/2003

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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	AA	5,014,276	5/7/1991	Bush et al.			
	AB	5,930,272	7/27/1999	Thesling			
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Foreign Patent Documents or Published Foreign Patent Applications

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	AE							
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Examiner Initial	Desig. ID	Document
	AJ	Wiberg, "Codes and Decoding on General Graphs", Dept. of Electrical Engineering, Sinkoping University, S-581 83 Sinkoping, Sweden, Dissertation No. 440 (1996)
	AK	Chugg et al., "Efficient architectures for soft-output algorithms", Communications, ICC 98, Conference Record, 1998 IEEE International Conference, Vol. 1, pp. 126-130 (1998)
	AL	Divsalar et al., "Hybrid Concatenated Codes and Iterative Decoding", TDA Progress Report 42-130, August 15, 1997
	AM	Berrou et al., "An IC for Turbo-Codes Encoding and Decoding", IEEE Solid-State Circuits Conference, Digest of Technical Papers, ISSCC 95, Session 5, Paper WP 5.7 (1995)
	AN	Hamkins, "Performance of Binary Turbo-Coded 256-ary Pulse-Position Modulation", TMO Progress Report 42-138, August 15, 1999
	AO	Berrou et al., "Near Shannon limit error-correcting coding and decoding: Turbo-codes", Communications, ICC 93 Geneva, Technical Program, Conference Record, IEEE International Conference, vol. 2, pp. 1064-1070 (1993)
	AP	Yiin et al., "MLSE and Soft-Output Equalization for Trellis-Coded Continuous Phase Modulation", IEEE Transactions on Communications, vol. 45, no. 6, June 1997, pp. 651-659
	AQ	Berrou et al., "Near Optimum Error Correcting Coding ad Decoding: Turbo-Codes", IEEE Transactions on Communications, vol. 44, no. 10, October 1996, pp. 1261-1271

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(37 CFR §1.98(b))		Applicant: Beerel, et al.	
		Filing Date: May 3, 2001	Group Art Unit: 2661

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U.S. Patent Documents

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	AL							
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	AQ	Benedetto et al., "Soft-Output Decoding Algorithms in Iterative Decoding of Turbo Codes", TDA Progress Report 42-124, February 15, 1996
	AR	
	AS	
	AT	

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	12/13/2003

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